

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
9 September 2005 (09.09.2005)

PCT

(10) International Publication Number
WO 2005/083945 A1

(51) International Patent Classification⁷: **H04L 12/46**

(21) International Application Number:
PCT/IB2005/050567

(22) International Filing Date: 14 February 2005 (14.02.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
04100681.8 20 February 2004 (20.02.2004) EP

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HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK,
LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ,
NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD,
SE, SG, SK, SL, SY, SZ, TJ, TM, TN, TR, TT, TZ, UA, UG,
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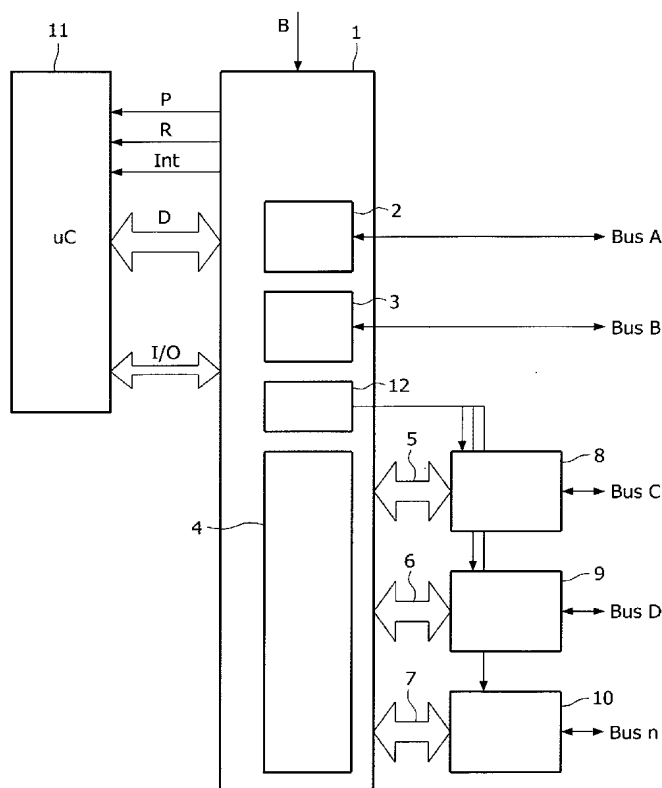
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BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CY, CZ,
DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM,

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,

[Continued on next page]

(54) Title: **SCALABLE SYSTEM CHIP FOR COUPLING DATA BUS SYSTEMS**



(57) **Abstract:** The invention relates to a scal-
able system chip (1) for coupling at least two
data bus systems, with at least one transceiver
(2; 3), integrated on the system chip, which is
provided to create a coupling with a data bus of
a first type, and with at least one controller (4),
integrated on the system chip, to control at least
one external transceiver (8; 9; 10), which is pro-
vided for coupling with a data bus of a second
type.

WO 2005/083945 A1



CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),

European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

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